



PSDB_DVI



GiDEL PSDB_DVI is a type 1 PSDB. This means that it uses a single connector to connect to the PROC motherboard. On the PROC boards, this connector is located to the **left** of the target FPGA.

PSDB_DVI is designed to provide a Digital Video Interface (DVI) connection to the PROC motherboard via its panel. Therefore, this daughterboard is intended to be placed on location 1 when possible. Nevertheless, it will work properly when connected to any other location on the PROC board.



GIDEL PSDB_DVI™ was designed to provide a simple and convenient way to connect an external camera to **GIDEL PROCStar II™** and **PROCSpark II™** boards.

PSDB_DVI key features include:

- ✓ Support DVI 1.0 with bandwidths of 25 to 225 MHz for wide range of display resolutions (VGA to QSGA).
- ✓ TMDS interface.
- ✓ Automatic detection by hardware / software.

PSDB_DVI uses two SD 29-pin female connectors (**J3** and **J4**), according the DVI standard.

The following adapters are used in PSDB_DVI to adapt the DVI Signals:

- Silicon Image **Sil1171** for DVI receiver.
- Silicon Image **Sil1172** for DVI transceiver