



PRESS RELEASE

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GIDEL ANNOUNCES PROCSTARIII™ FAMILY OF RECONFIGURABLE, EMBEDDED COMPUTE ACCELERATOR SYSTEMS

*Altera Stratix III-based Systems Support PCIe X8, 1G Ethernet Host Communications
and Standalone Operation*

October 31, 2007. Or Akiva, ISRAEL. GiDEL, a leading supplier of FPGA-based compute accelerator systems today announced the new PROCStarIII™ family of products. The product family is targeted at machine vision, imaging, and high performance, embedded computing applications. The system's Stratix III FPGAs provide reconfigurable hardware implementation of users' algorithms to deliver application specific processing acceleration. The system also delivers very high speed SoC prototyping and debugging for pipelined dataflow intensive SoCs.

The PROCStarIII can be configured with a broad range of Stratix III FPGAs to match both the computational capacity and speed of the system to user's requirements. The Stratix III E FPGAs are rich in arithmetic and memory elements and ideal for DSP and memory intensive applications such as image and signal processing. The Stratix III L-FPGAs are ideal for SoC prototyping. The PROCStarIII is CameraLink registered as a Frame Grabber and Image Processing product, like the PROCStarII™, PROCSparkII™ and PROCe™ systems.

“GiDEL's PROCStarIII development system gives designers an enormous amount of flexibility and resources for system development,” said Danny Biran, senior vice president of product and corporate marketing for Altera. “The combination of our Stratix III FPGAs and GiDEL's board architecture enables GiDEL to deliver a development system that will meet the requirements of almost any designer.”

Key Features of the PROCStarIII are as follows:

- 1-4 Stratix III 80E, 110E, 260E or 150L FPGAs
- 8 lanes of PCI Express for 2GB/s bandwidth host communications
- 8 SODIMM slots for up to 16 GB of on board memory
- 256MB DDR2 memory per FPGA (1GB DDR2 max.)
- 1 G Ethernet host communications

- Support for up to five application specific PROC daughter cards—CameraLink, DVI, Gigabit Ethernet, etc.
- Typical system clock speeds of 150-350 MHz
- High-performance system-to-system interconnection
- Standalone operation

The PROCStarIII system is available to ship in the fourth quarter of 2007.

“Altera’s Stratix III FPGAs enable our PROCStarIII compute accelerator system to achieve leading-edge performance and maximum flexibility to fit the high performance computing needs of our customers,” said Reuven Weintraub, president and ceo, GiDEL.

The PROCStarIII system is easily configured for machine vision, automated optical inspection, medical image processing, high performance computing, video processing and communications applications. With the PROC Developer’s Kit, there is no need to design a custom board, a PCI Express driver, an application driver layer, define board constraints, design memory controllers or write host interface code. These are all delivered with the product or generated with the developer tools. This saves months of effort and enables designers to focus on their proprietary value-added design.

Optional PROC_HILs™ software automates the FPGA implementation of algorithms created with model-based tools like MathWorks’ HDL Coder and Altera’s DSP Builder. PROC_HILs also automatically generates the wrappers to allow the PROCStarIII to accelerate Simulink simulations by replacing the original software based algorithm models with the PROCStarIII.

Configurations and pricing can be obtained by contacting GiDEL.

About GiDEL.

GiDEL Ltd. is a successful, profitable and innovative company which was founded in 1993. GiDEL has become one of the market leaders as a company that continuously provides cutting-edge reconfigurable technology utilizing FPGAs. GiDEL sees it's customers as partners and uses its vast experience at the project-level and FPGA design to focus on its customers' projects' success. Customers in semiconductor, consumer product, communications, machine vision, medical imaging, and military/aerospace markets purchase the PROC family of reconfigurable PROCessors (1) for SoC and ASIC verification, (2) as COTS (Commercial Off-The-Shelf) acquisition and accelerator boards, and (3) to validate complex algorithms. For more information, contact GiDEL in North America at 408-969-0389, or worldwide at +972 4 610 2505, or on the web at www.gidel.com.

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