



PRESS RELEASE

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Contact:

GiDEL-North America, Ralph Zak, Director, North American Sales, 408-969-0389,

z_ralph@gidel.com

GiDEL Worldwide: Shlomo Keisari, WW VP of Sales and Marketing, +972 4 610 2500,

k_shlomo@gidel.com

GIDEL ANNOUNCES CAPACITY INCREASE FOR PROCSTARIII™ FPGA BOARD FOR SOC PROTOTYPING AND ASIC EMULATION

Altera's Stratix III 340L FPGA Now Available

February 18, 2008. Or-Akiva, Israel. GiDEL, a leading supplier of FPGA-based compute accelerator systems, today announced that the new PROCStarIII™ family of products is now available with the largest capacity and fastest FPGA, the Stratix III 340L from Altera. With four such FPGA devices, pipelined SoC designs up to 12 million ASIC gates in size can be prototyped and verified. Multiple systems can be combined for verifying even larger designs.

“Altera’s Stratix III FPGAs enable our PROCStarIII system to achieve leading-edge performance and maximum flexibility to fit the SoC verification needs of our customers,” said Reuven Weintraub, President and CTO, GiDEL.

Key Features of the PROCStarIII are as follows:

- 1-4 Stratix III 80E, 110E, 260E, 150L or 340L FPGAs
- 8 lanes of PCI Express for 2GB/s bandwidth host communications
- 8 SODIMM slots for up to 16 GB of on board memory
- 256MB DDR2 memory per FPGA (1GB DDR2 max.)
- 1 G Ethernet host communications
- Support for up to five application specific PROC daughter cards—CameraLink, DVI, Gigabit Ethernet, etc.
- Typical system clock speeds of 150-350 MHz
- High-performance system-to-system interconnection
- Standalone operation

GiDEL’s PROCWizard Software and available integrated third party applications provide and an easy-to-use environment for design partitioning, FPGA implementation and design debug.

“GiDEL’s PROCStarIII development system gives designers an enormous amount of

flexibility and resources for system development,” said Danny Biran, senior vice president of product and corporate marketing for Altera. “The combination of our Stratix III FPGAs and GiDEL’s board architecture enables GiDEL to deliver a development system that will meet the requirements of almost any designer.”

The system’s Stratix III FPGAs can also provide reconfigurable hardware implementation of complex algorithms to deliver embedded application specific processing acceleration. A broad range of Stratix III FPGAs are available to match the computational capacity and speed of the system to user’s requirements. The PROCStarIII is CameraLink registered as a Frame Grabber and Image Processing product, like its sister products, the PROCStarII™, PROCSparkII™ and PROCe™ systems.

Configurations and pricing can be obtained by contacting GiDEL.

About GiDEL.

GiDEL Ltd. is a successful, profitable and innovative company which was founded in 1993. GiDEL has become one of the market leaders as a company that continuously provides cutting-edge reconfigurable technology utilizing FPGAs. GiDEL sees it's customers as partners and uses its vast experience at the project-level and FPGA design to focus on its customers' projects' success. Customers in semiconductor, consumer product, communications, machine vision, medical imaging, and military/aerospace markets purchase the PROC family of reconfigurable PROCessors (1) for SoC and ASIC verification, (2) as COTS (Commercial Off-The-Shelf) acquisition and accelerator boards, and (3) to validate complex algorithms. For more information, contact GiDEL in North America at 408-969-0389, or worldwide at +972 4 610 2505, or on the web at www.gidel.com.

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