

# **GiDEL Announces Its Third-Generation ASIC Prototyping Systems Based on Altera's Stratix IV E FPGAs**

PROC\_SoC ASIC Prototyping Systems

Feature Industry's Largest FPGAs Currently Shipping

June 14, 2010, Anaheim, CA –GiDEL announced today the availability of the PROC\_SoC3-4S™ and the PROC\_SoC10-4S™ ASIC Prototyping Systems utilizing the industry's largest FPGAs – Altera's Stratix® IV E FPGA featuring 820K logic elements (LEs).

The PROC\_SoC3 and PROC\_SoC10 are GiDEL's third-generation ASIC Prototyping Systems. Featuring the industry's largest and fastest FPGA, the Stratix IV E FPGA-based PROC-SoC ASIC Prototyping Systems are designed to debug and verify today's most advanced SoC designs. The PROC\_SoC3-4S is able to support designs up to 36 million ASIC gates and the PROC\_SoC10-4S is able to support designs up to 120 million ASIC gates. By leveraging the system's flexible architecture, both ASIC prototyping systems can be connected to provide support for up to 360 million ASIC gates. The systems are architected and designed to operate at system clock speeds up to 300MHz.

The PROC\_SoC ASIC Prototyping Systems set a new standard for ASIC verification, ASIC prototyping, system performance, price/capacity, interconnect flexibility and ease-of-use. The systems offer an organized chassis which includes user add on logic, boards, management software and advanced debug tools.

“We are pleased to base our next-generation ASIC Prototyping Systems on Altera's Stratix IV E FPGA,” said Reuven Wientraub, GiDEL’s President and CTO. “Combining the industry's highest density FPGA shipping today with our flexible PROC\_SoC prototyping environment enables us to provide the most advanced, highest-capacity solution to our customers.”

“GiDEL has been a strong partner of Altera for many years and has always leveraged our most advanced FPGAs,” said Jim Smith director of professional services, training and boards, commented at Altera. “Today almost every new ASIC project uses FPGAs for ASIC prototyping and we are committed to providing the industry's highest density devices to support this market. GiDEL’s PROC\_SoC systems provide advanced prototyping solutions with a powerful combination of high-performance, high-density FPGAs and advanced software tools.”

GiDEL will be presenting the new PROC\_SoC3-4S and PROC\_SoC10-4S ASIC Prototyping Systems at this year’s Design Automation Conference in Anaheim on June 14-16, at booth 912.

**About GiDEL**

GiDEL is the first company to introduce ASIC Prototyping Systems and has been leading the ASIC Prototyping technology for over a decade with its cutting-edge architectures, solutions and methodologies. Its family of PROC\_SoC ASIC Prototyping Systems with its patented technology allowing any FPGA to directly connect to any other FPGA enables the highest performance in prototyping, and its new TotalHistory debug feature provides a new dimension to prototyping debug.

For more information please see [www.gidel.com/ASIC-Prototyping](http://www.gidel.com/ASIC-Prototyping)

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